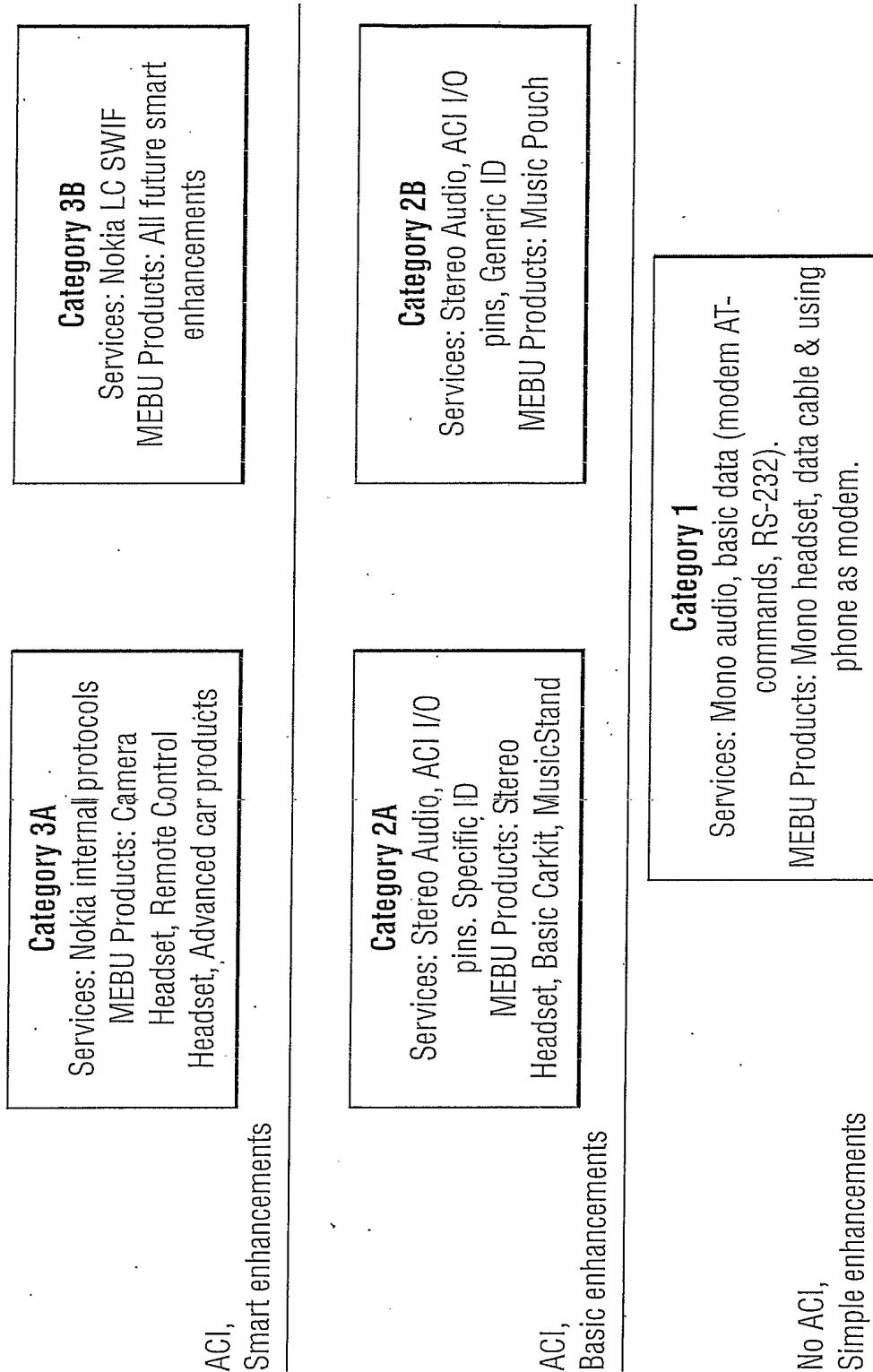


17

FIG. 1

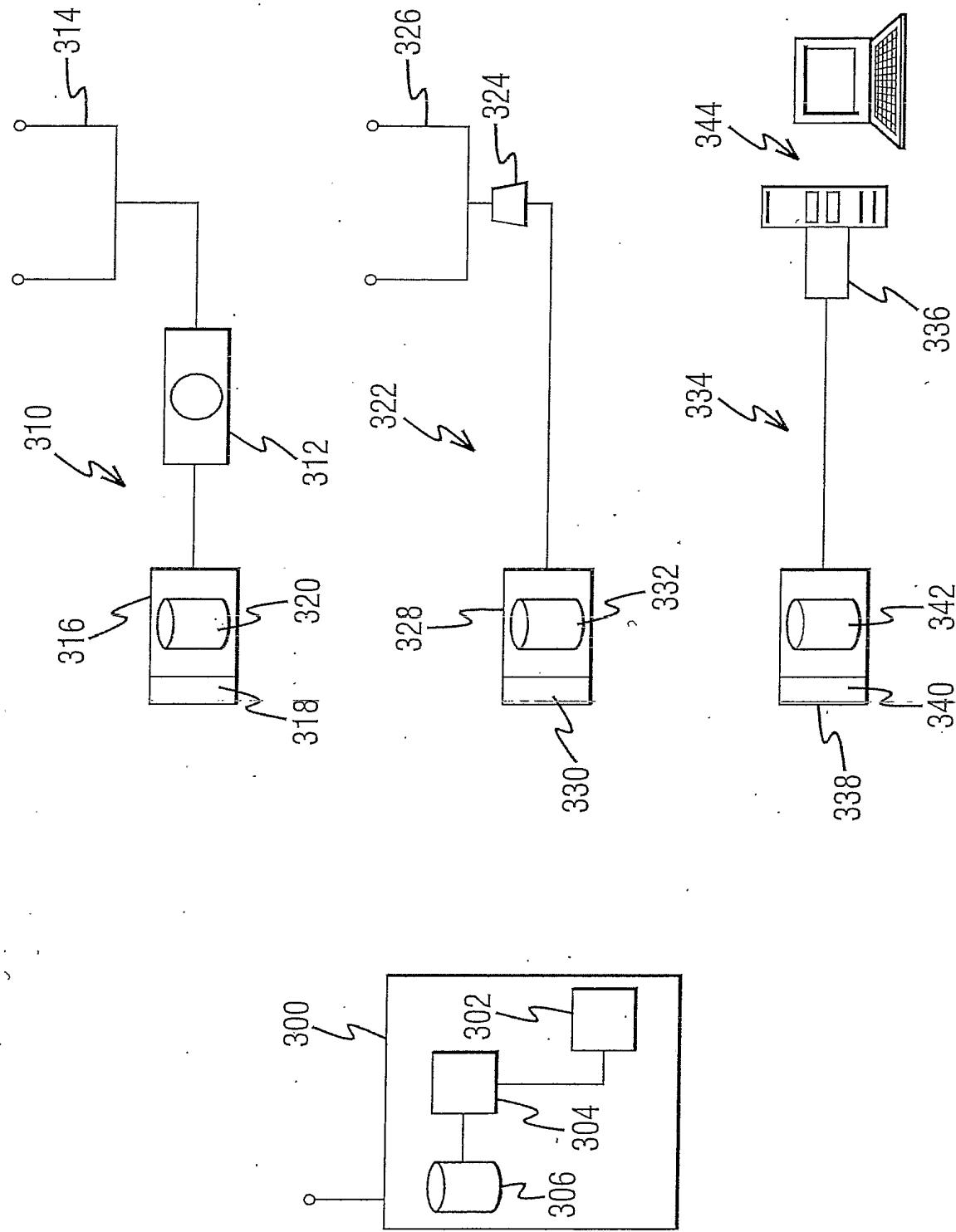
System Block		Address	Field	Value	Size	Bit						Notes
Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0					
		+0	BlockID	1 byte								Block ID
		+1	DataLength	1 byte								Byte count from +2, parity byte not included
		+2	<u>Acc_type high</u>	1 byte								
			MasterType	6 bit					#	#		MasterType
			SubType MSB	2 bit	#	#	#	#	#	#		SubType high
		+3	<u>Acc_type low</u>	1 byte								
			SubType LSB	3 bit				#	#	#	#	SubType low
			Acc_ID	5 bit	#	#	#					Accessory identification number
		+4	Parity byte	1 byte								Odd parity for each bit

FIG. 2



3/7

FIG. 3



4/7

FIG. 4

Generic ID System Block		Field	Value	Size	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	Notes
+0	<u>Block_ID</u>		4 bits					#	#	#	#	#	Block ID
	<u>Device_ID</u>		12 bits		#	#	#	#					
													Device_ID
+2	<u>Resource_ID</u>		1 byte										
+3	<u>Device_type</u>		3 bits					#	#	#	#	#	Device type
	<u>Spare</u>		5 bits		#	#	#						Spare
+4	<u>I/O_Inf</u>		8 byte										
					1 bit		#	#	#	#	#	#	Feature active state (0/1)
													Feature for I/O-pin (0...7)
+12	<u>I/O input/output</u>		1 byte										"1" = Output, "0" = Input

5/7

FIG. 5

Resource_ID field	
Bit	Sub field
0	NBUS
1	FBUS
2	USB
3	Audio
4	Music playback
5	FM antenna
6	Spare
7	Spare

6/7

FIG. 6

Audio Block		Value	Size	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	Notes
Address	Field											
+14	Uplink sensitivity		5 bits						#	#	Uplink sensitivity (Mic Gain)	
			3 bits	#	#	#	#	#			AEC table selection	
+15	Downlink volume		5 bits						#	#	Downlink sensitivity (Output Gain)	
			3 bits	#	#	#	#	#			Max output level	
+16	Enhancement Switchers		1 bit	#	#	#	#	#	#	#	Up alwe on/off	
			1 bit	#	#	#	#	#	#	#	Down alwe on/off	
			2 bits	#	#	#	#	#	#	#	Output switcher	
			1 bit	#	#	#	#	#	#	#	Mic on/off	
			2 bits	#	#	#	#	#	#	#	DRC target value	
+17	Parametric wideb EQ	9 bytes	1 bit	#	#	#	#	#	#	#	Input impedance	
+31	Parity byte	1 byte									Odd parity for each bit over the whole block	

7/7

FIG. 7

